Talk from Kyushu Institute of Technology, Fukuoka Japan

Co-sponsored by the United Kingdom and Ireland Chapter of the IEEE Solid-State Circuits Society and the SFI CONNECT Centre for Future Networks

Satoshi Matsumoto of Kyushu Institute of Technology, Fukuoka Japan gave a guest talk at Tyndall this week. The talk was entitled R&D for 3D-Power SoC in Japan.

Satoshi Matsumoto Bio
Satoshi Matsumoto received B.S. degree, M.S. degree, and Dr. Eng. degree in applied chemistry from Waseda University, Tokyo Japan, in 1982, 1984, and 1996, respectively. In 1984, he joined NTT (Nippon Telegraph and Telephone Corporation) LSI Laboratories where he worked on the research and development of SOI Bi-CMOS devices, power ICs, analog high frequency devices and circuits, and energy harvesting systems. In 2010, he moved to Kyushu Institute of Technology, Fukuoka Japan, as a professor. His research interests are 3D integrated circuits and systems and energy harvesting technology.